

FOUO 505050

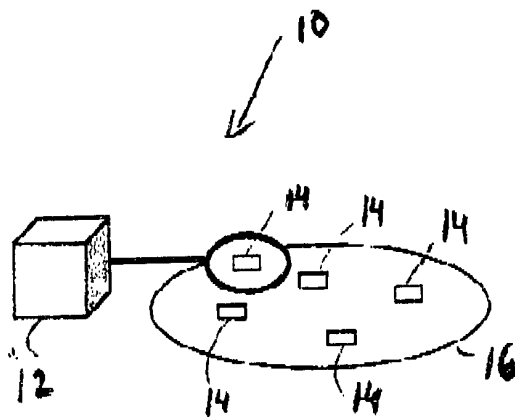


FIGURE 1

FORM 50-504560

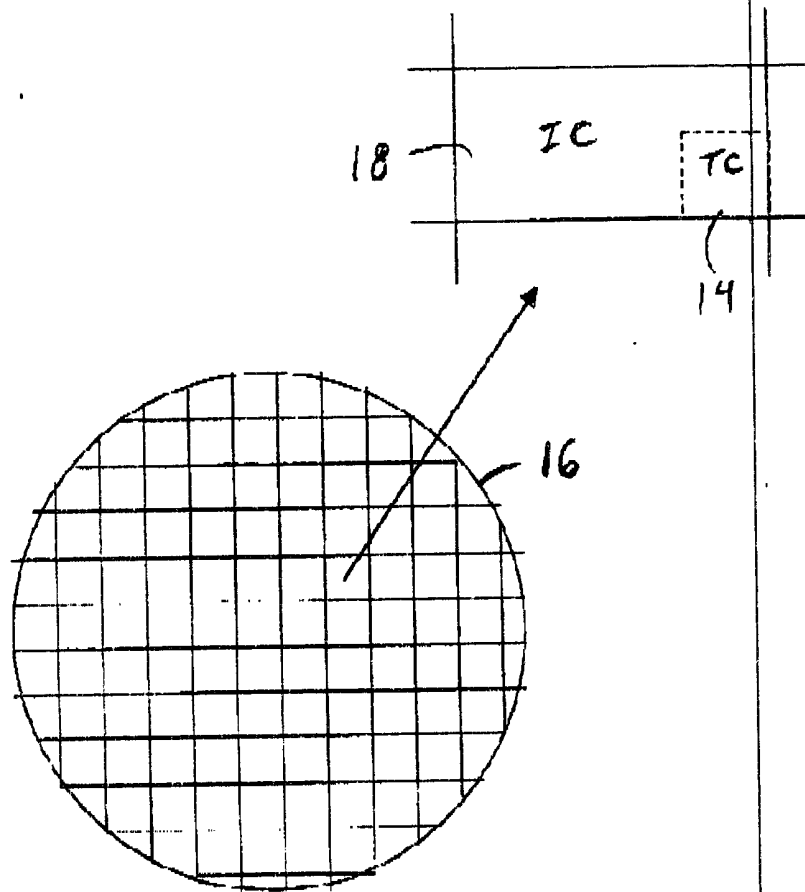


FIGURE 3

FOI 50-50649860

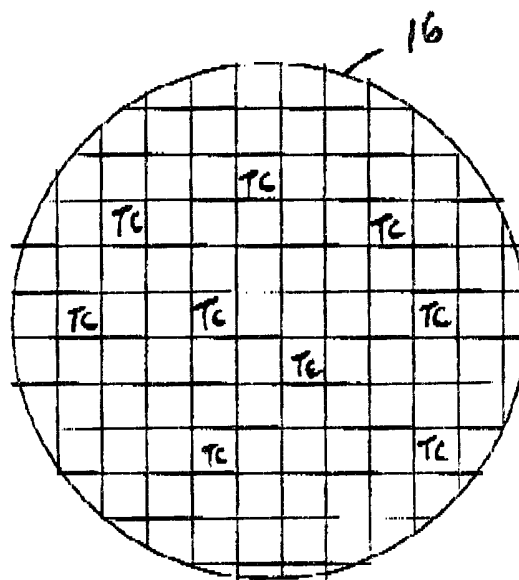


FIGURE 4

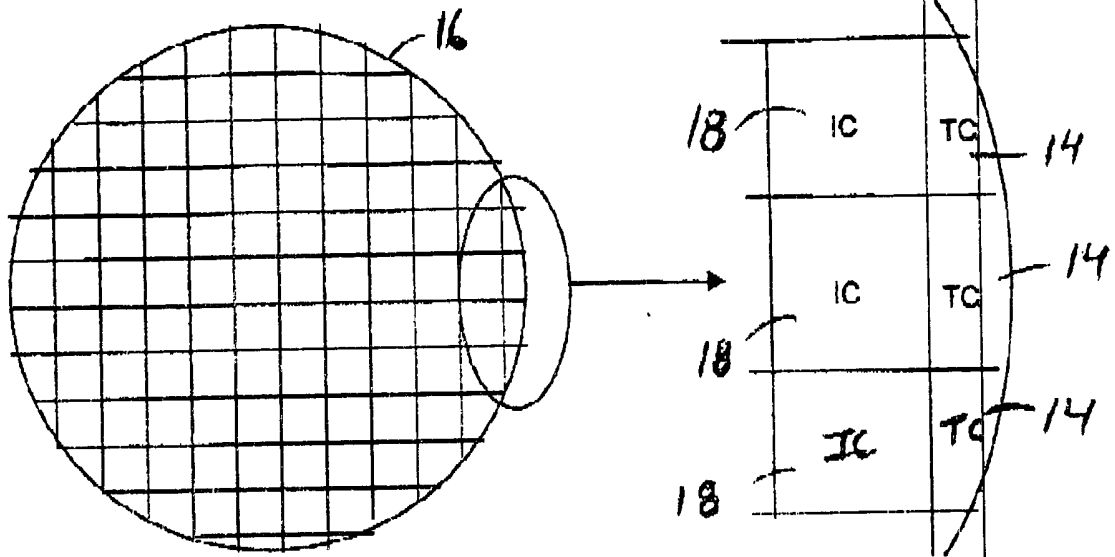


FIGURE 5

1051650-90645880

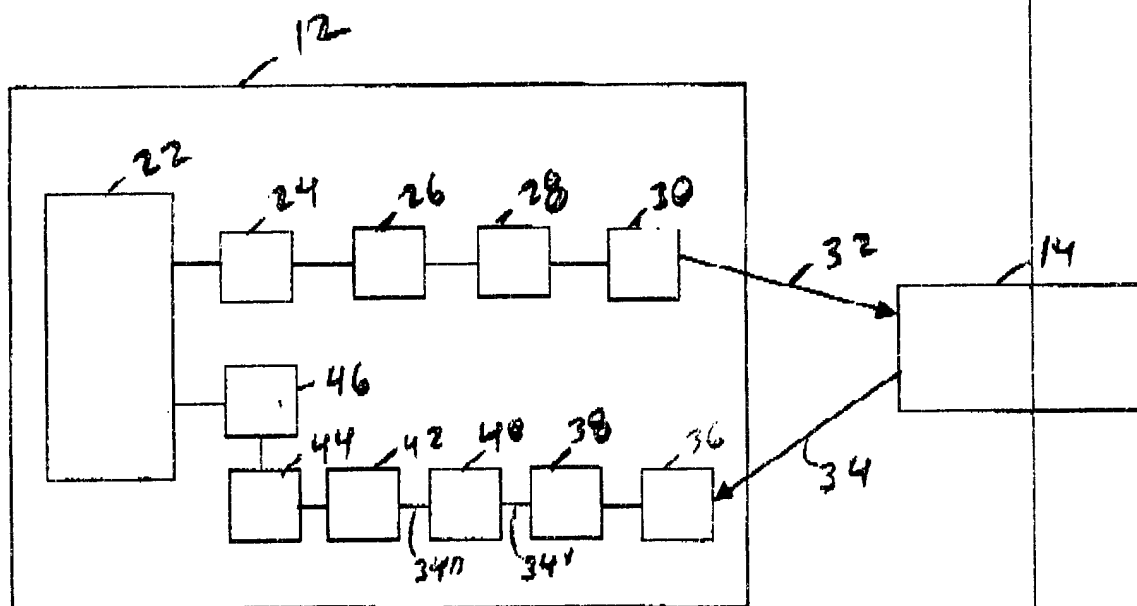


FIGURE 6

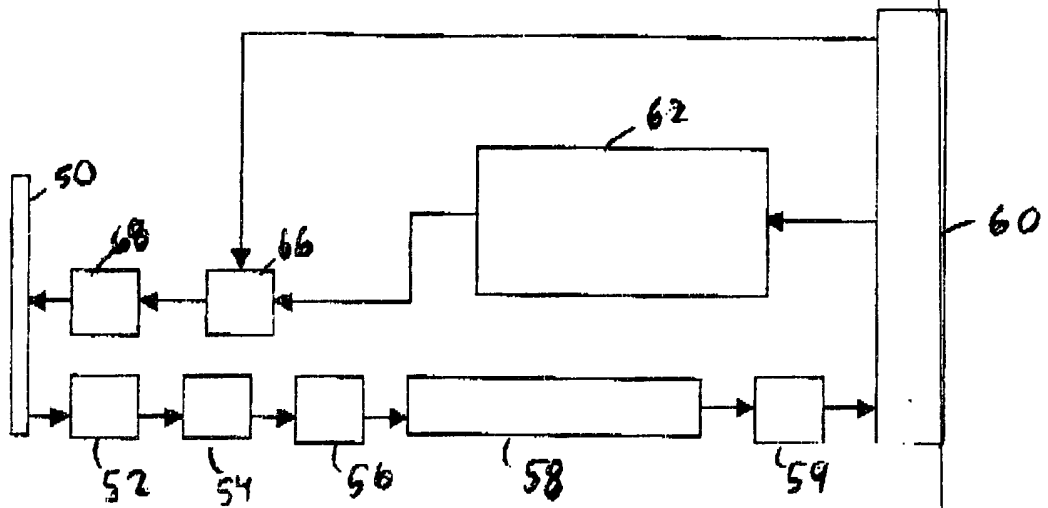


FIGURE 7.

105750-5064560

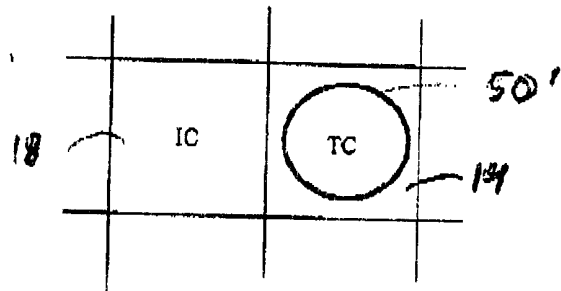


FIGURE 8

TOP SECRET

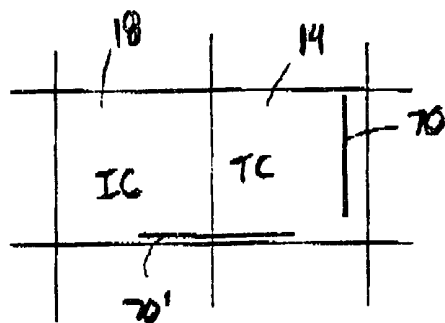


FIGURE 9a

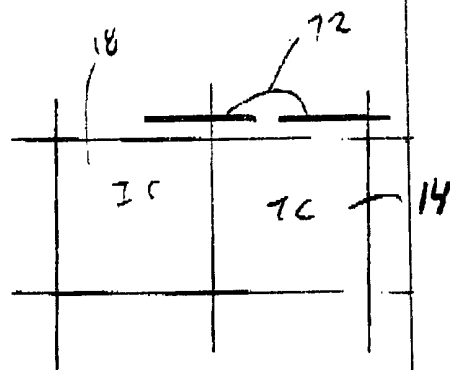


FIGURE 9b

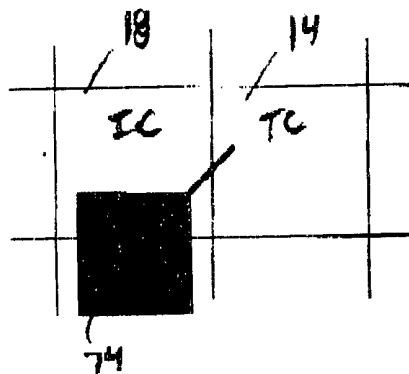


FIGURE 9c

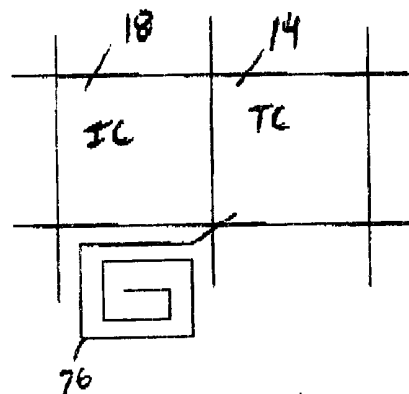


FIGURE 9d

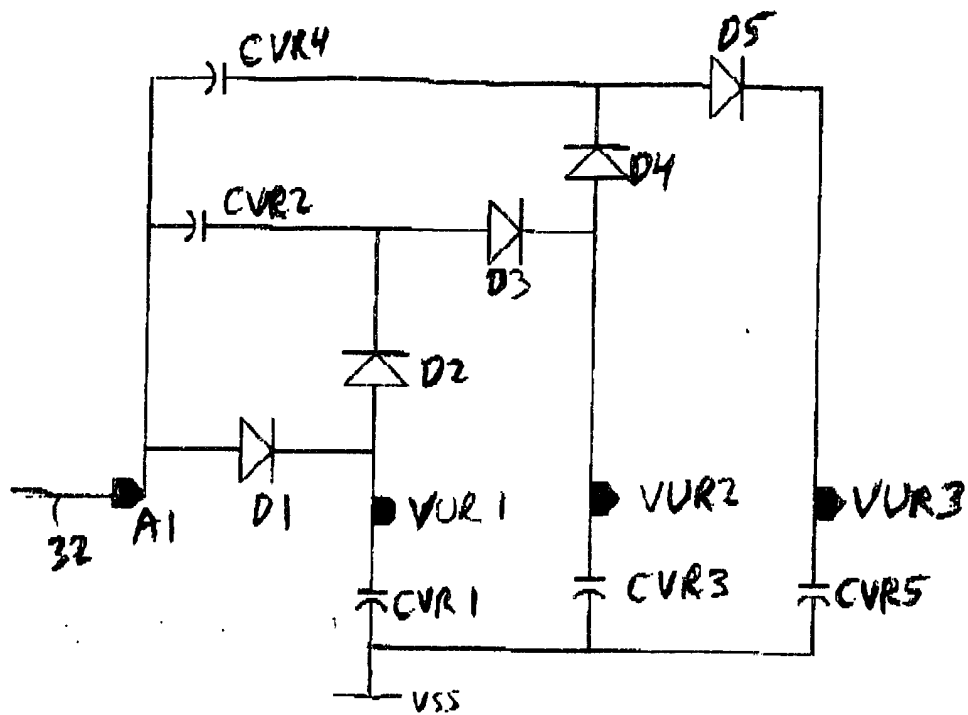


FIGURE 10.

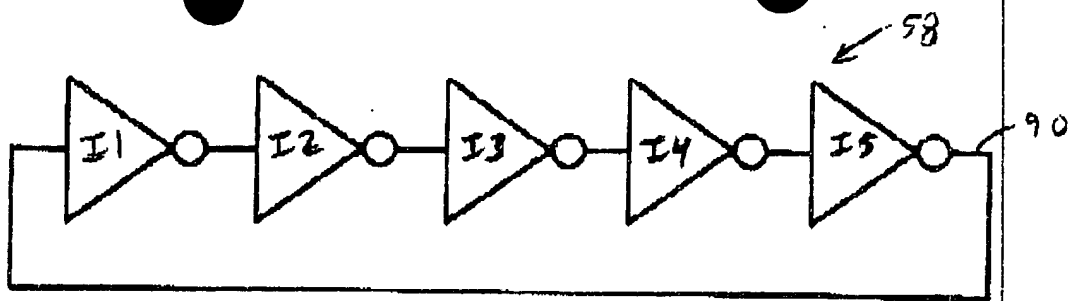


FIGURE 12

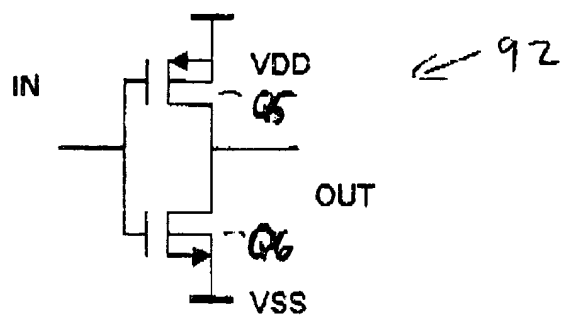


FIGURE 13

FIGURE 13

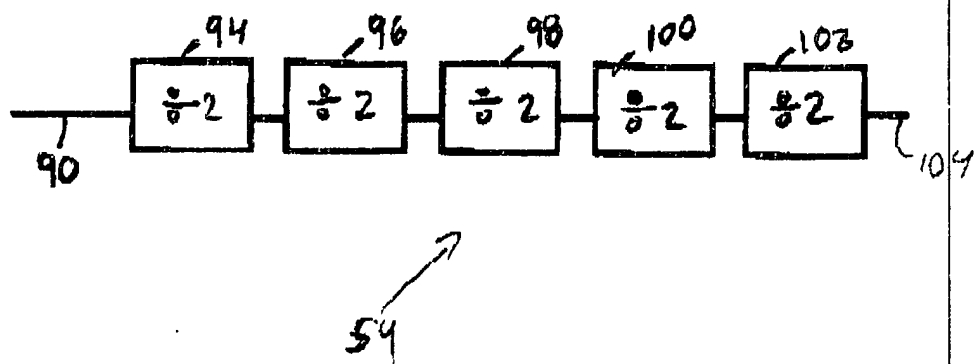


FIGURE 14.

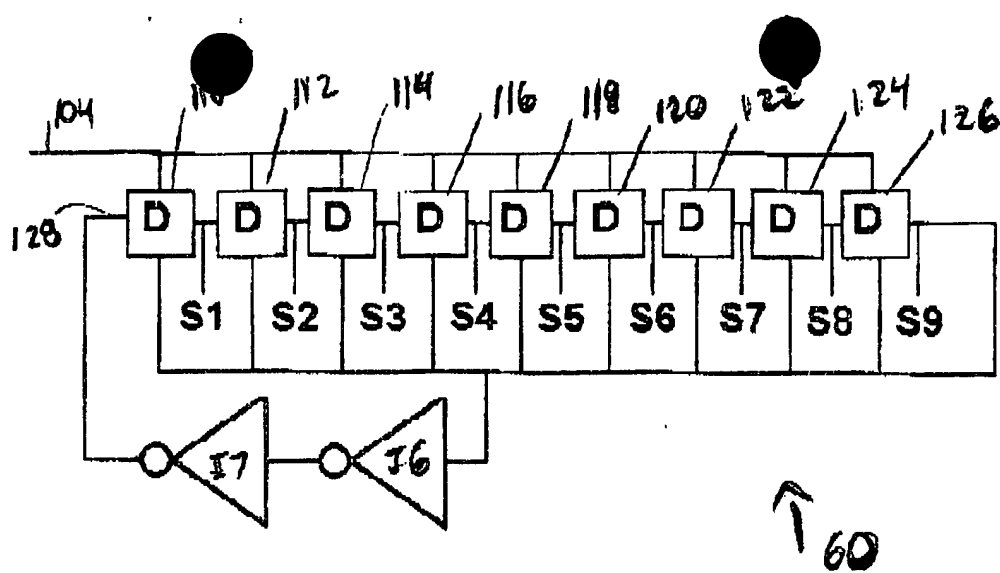


FIGURE 15

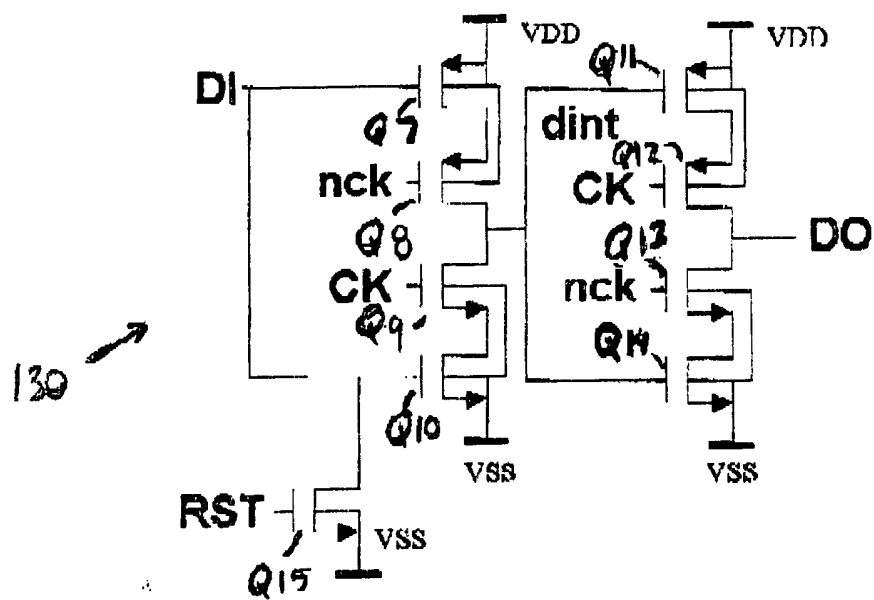


FIGURE 16

FIG. 150 5064560

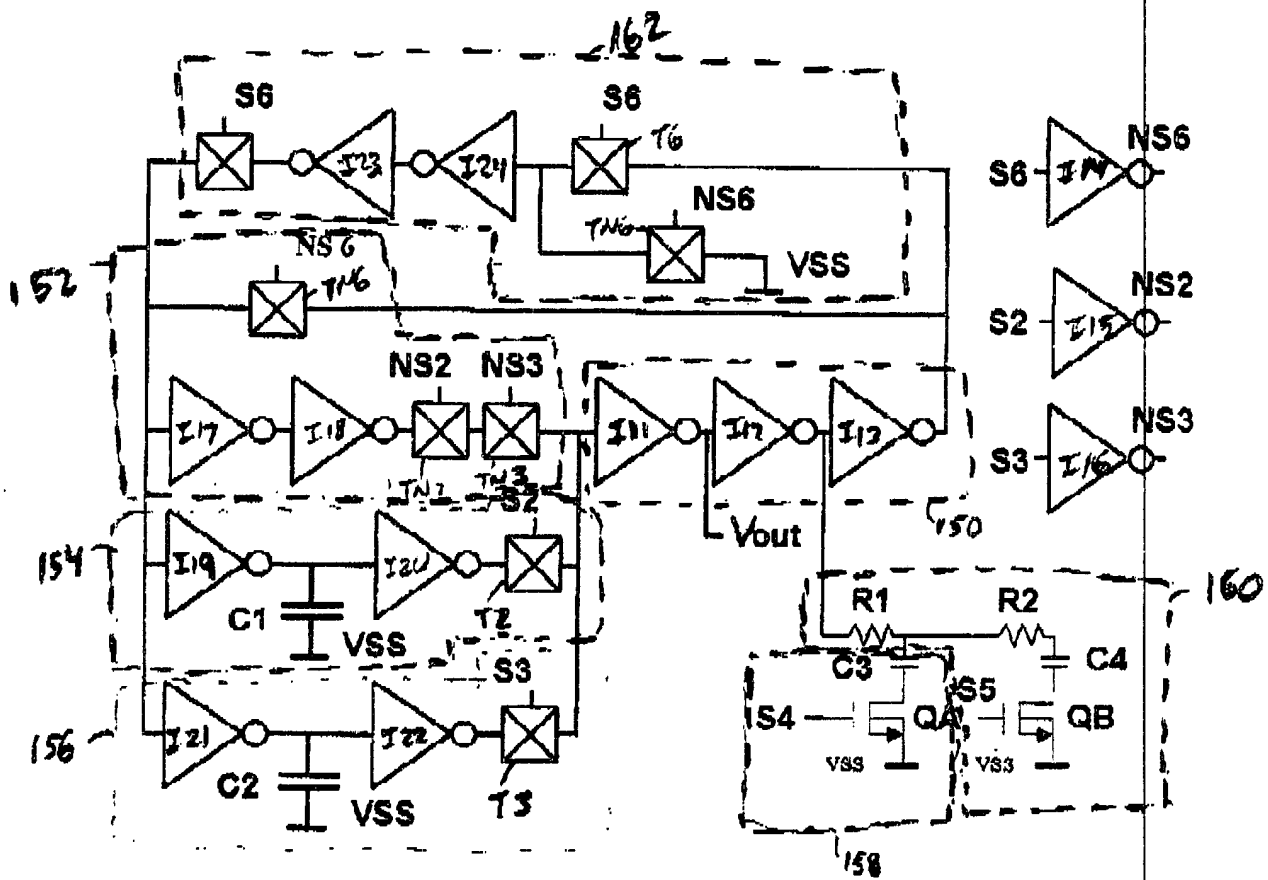


Figure 18

62

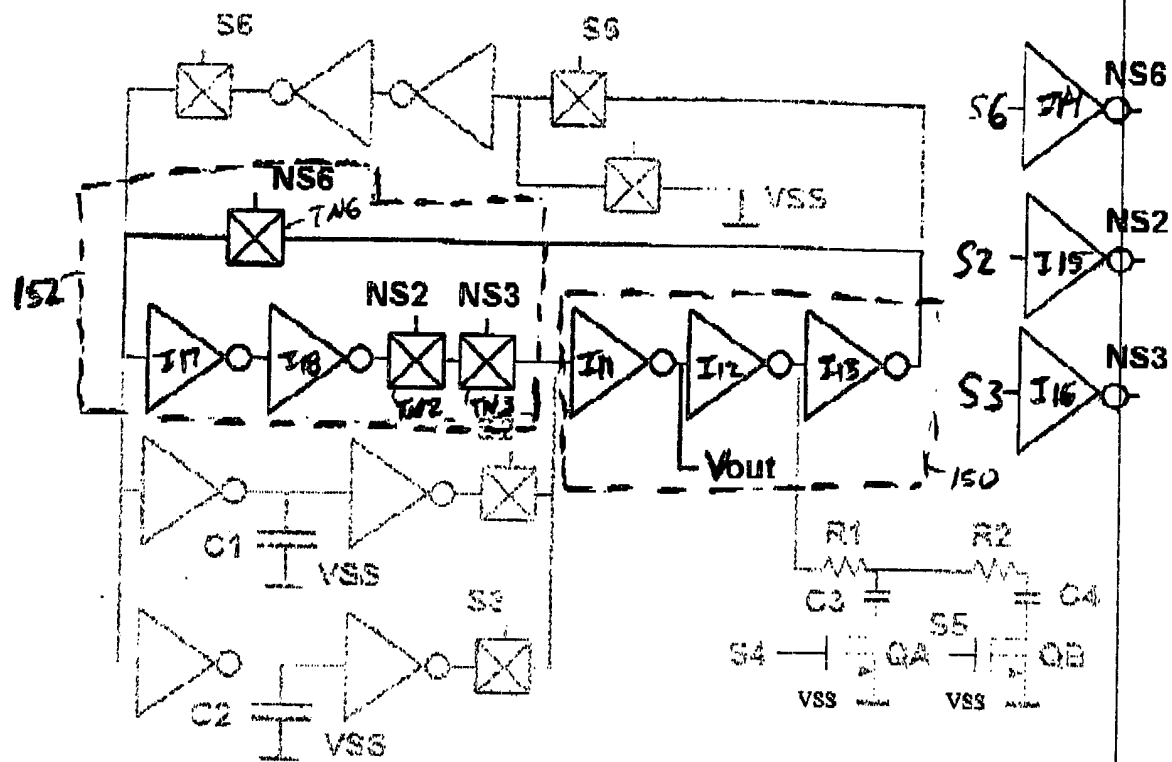


FIGURE 20

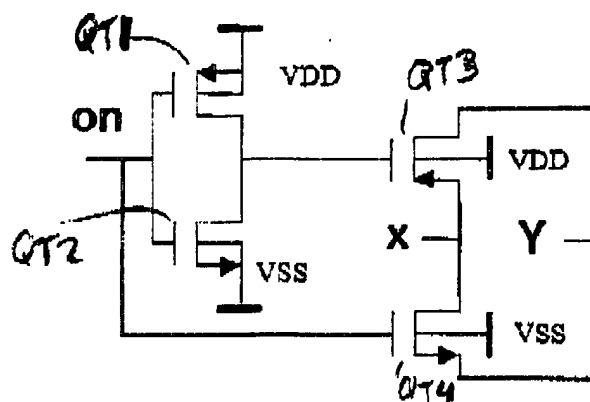


FIGURE 19

FIGURE 19

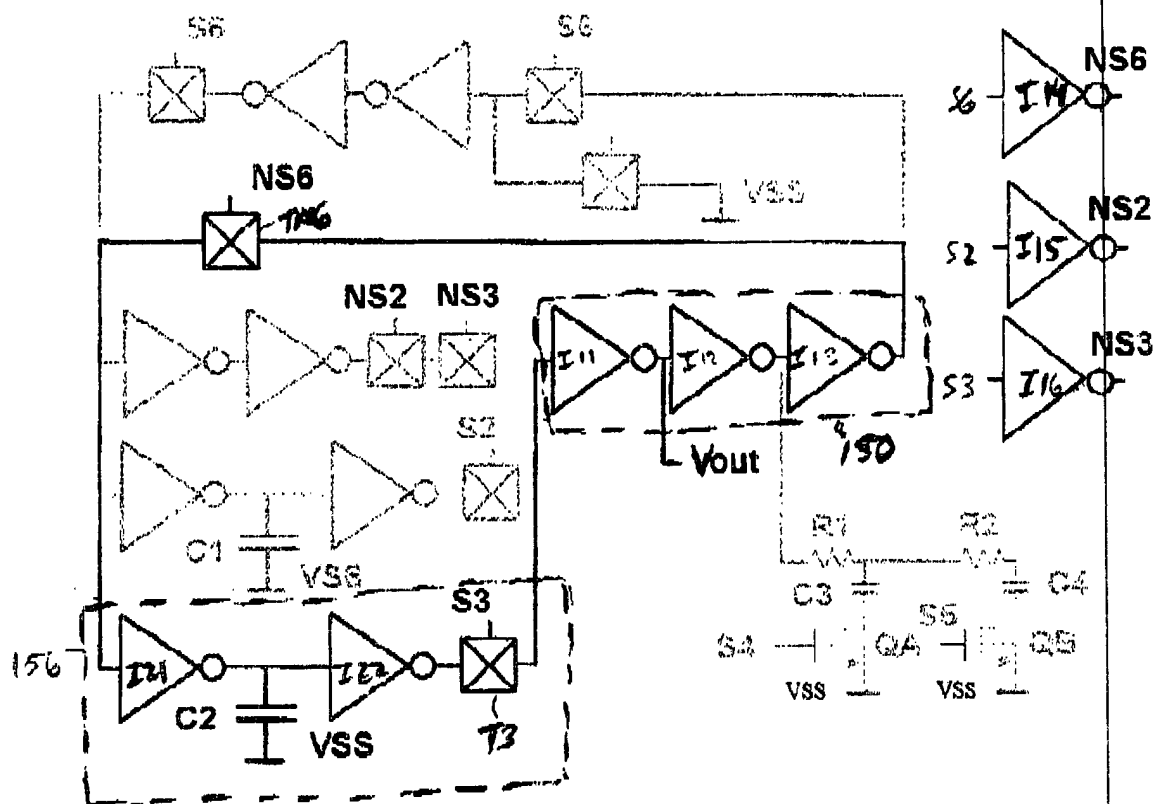


FIGURE 22

FIGURE 21

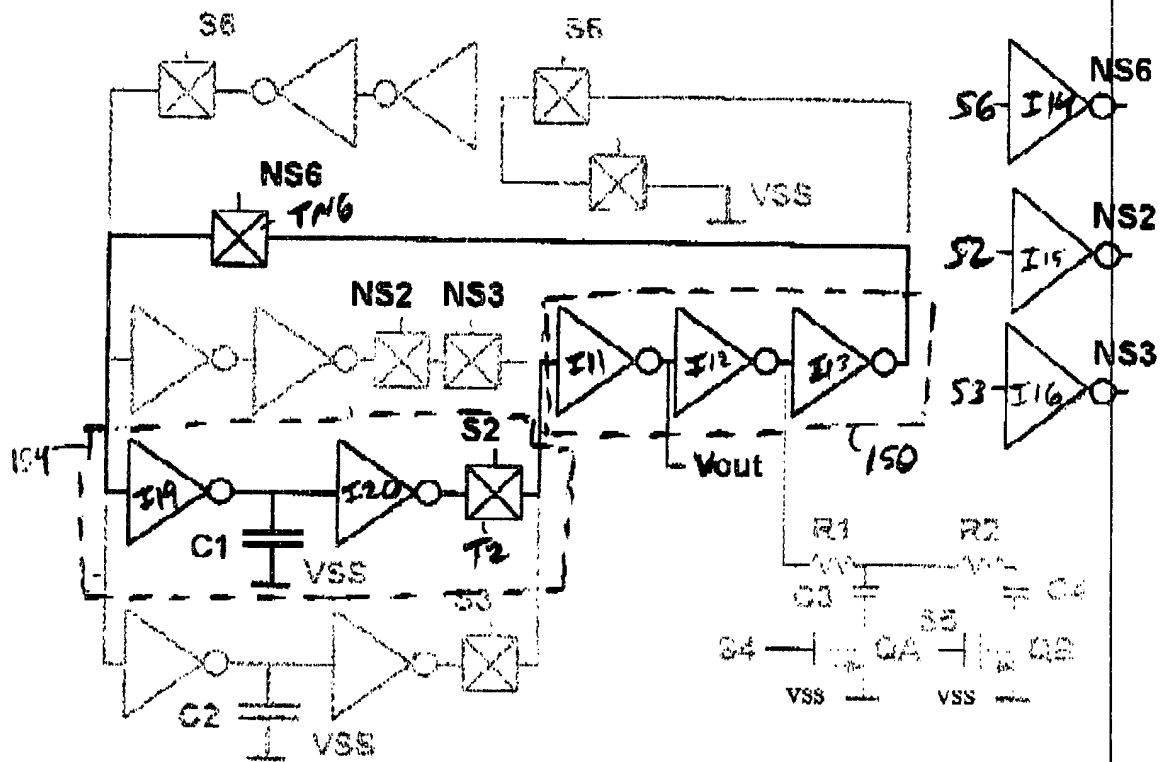


FIGURE 21

FOI50"50645660

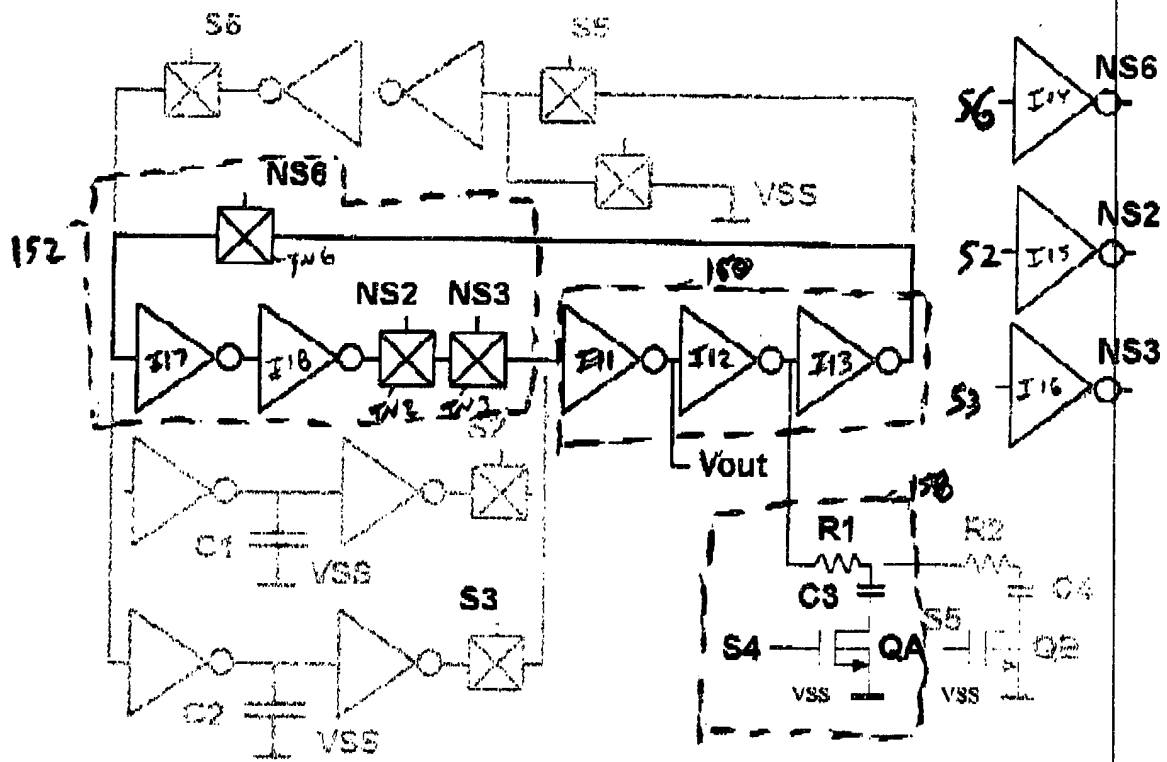


FIGURE 23

FIGURE 24

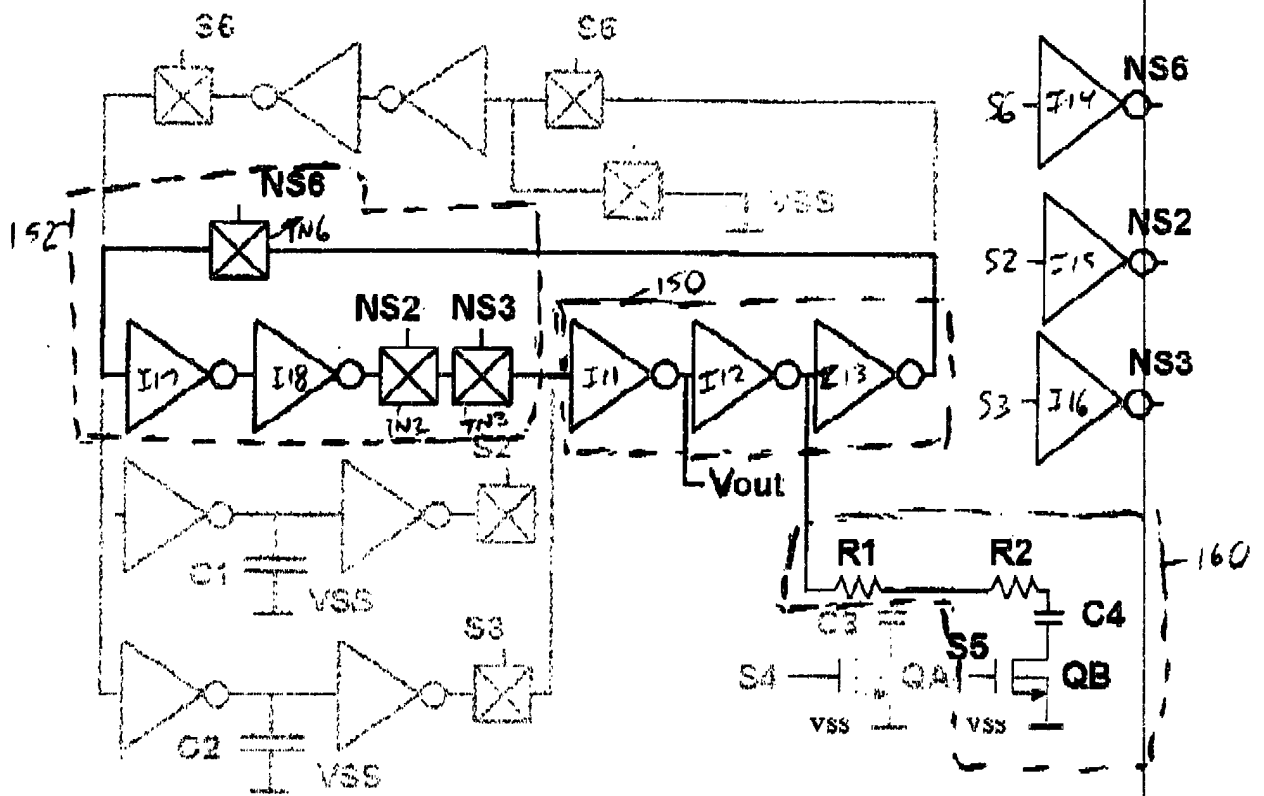


FIGURE 24

[illegible]

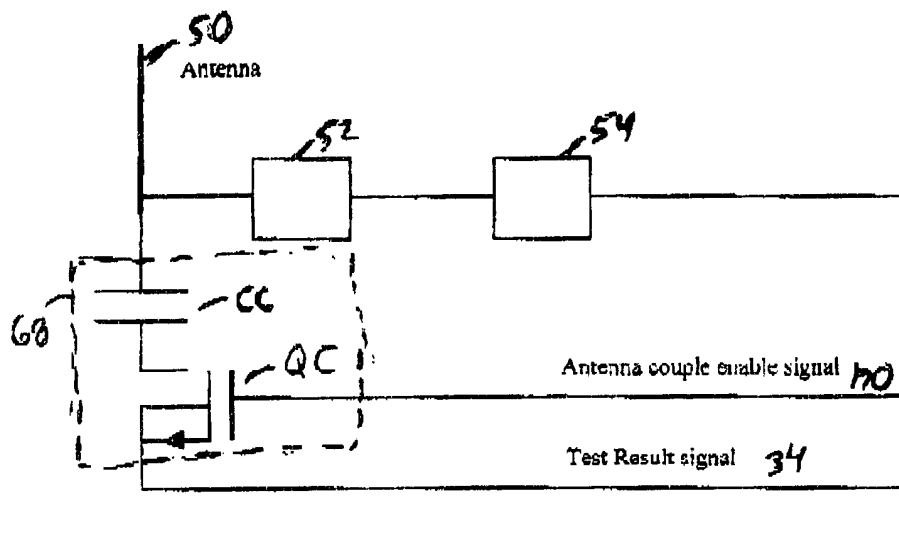


FIGURE 26

FIGURE 26

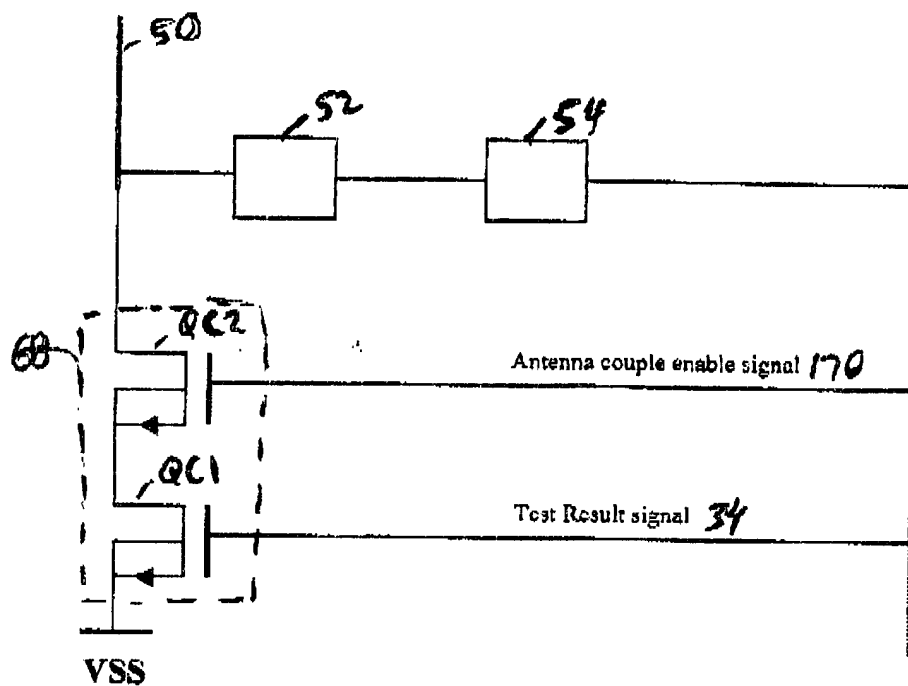


FIGURE 27

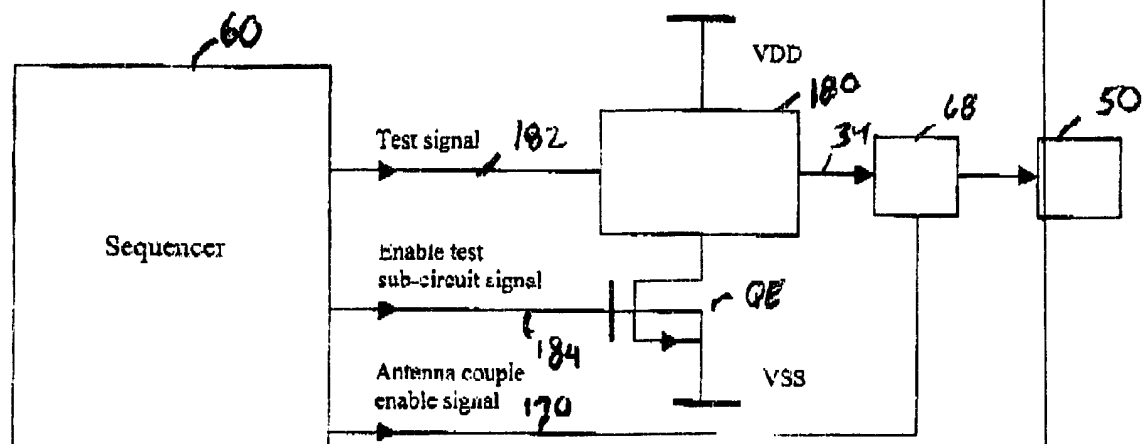


FIGURE 28

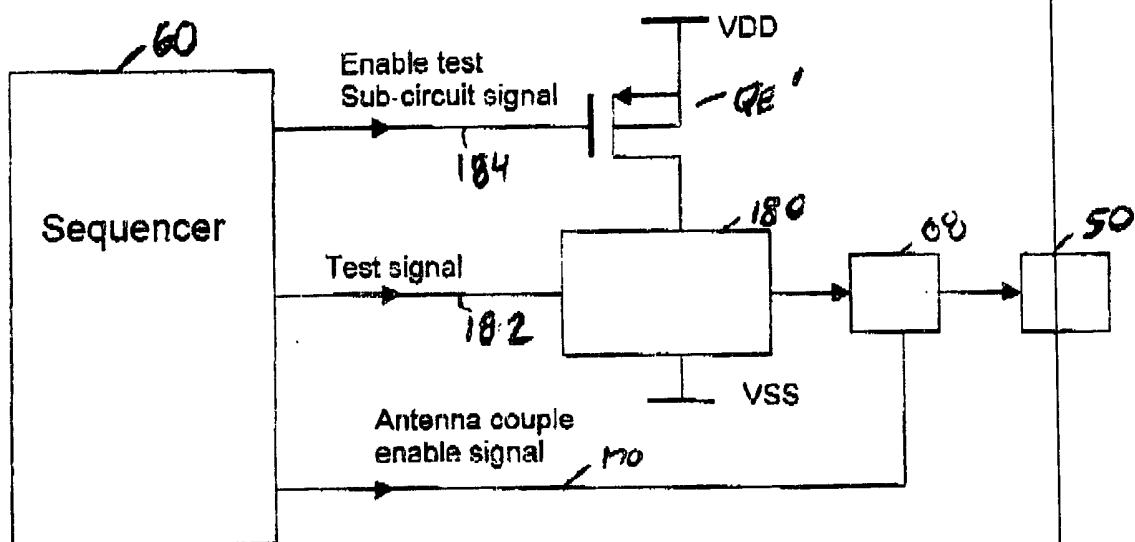


FIGURE 29

FIGURE 29

bm25 1:9_testa schematic : Apr 11 22:46:58 2020
Transient Response

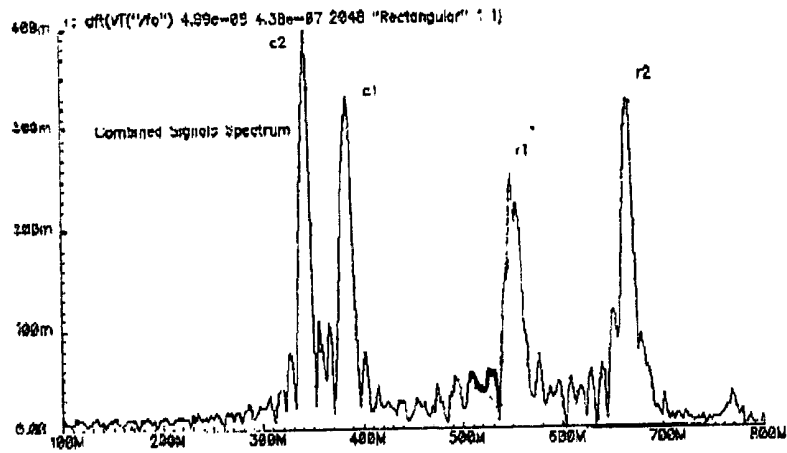


FIGURE 30a

FIGURE 30a

sc982 31/vvoo schematic : Mar 26 19:55:38 2000

Transient Response

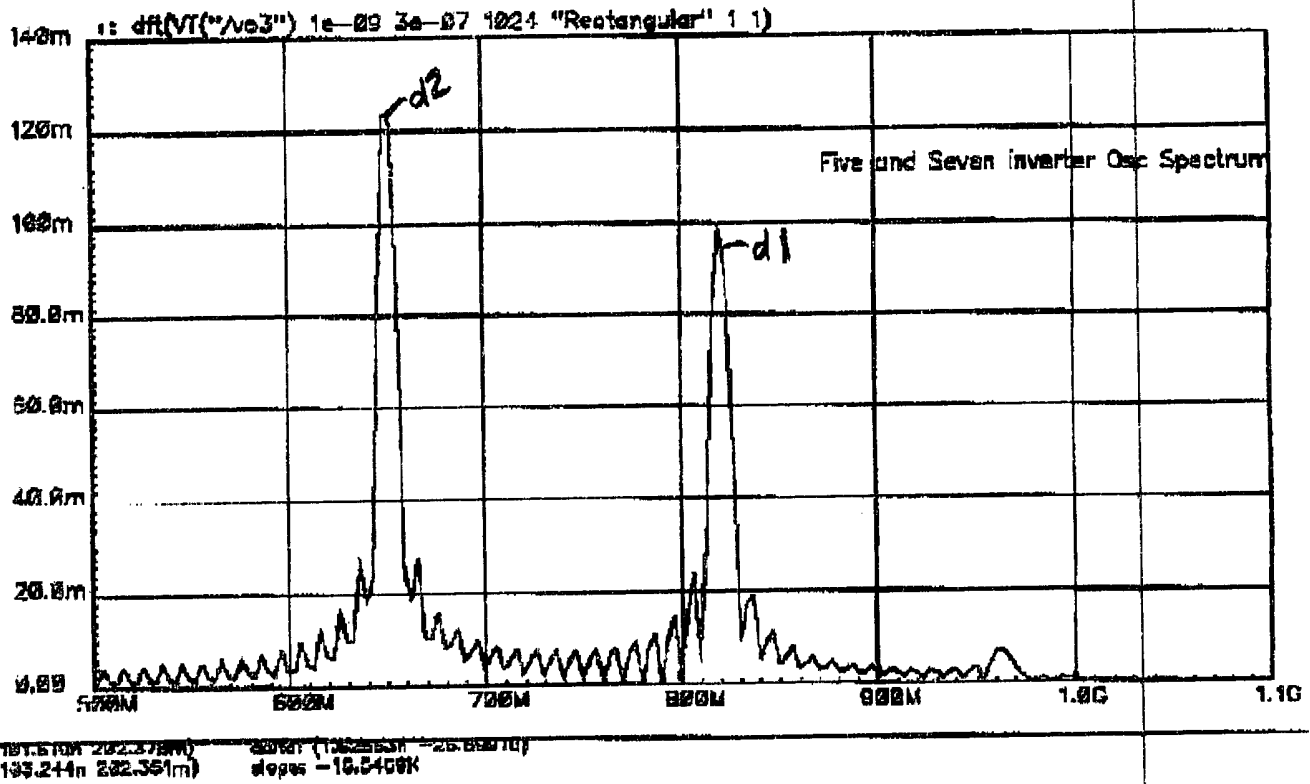


Figure 30b

5 ring osc Frequency vs technology

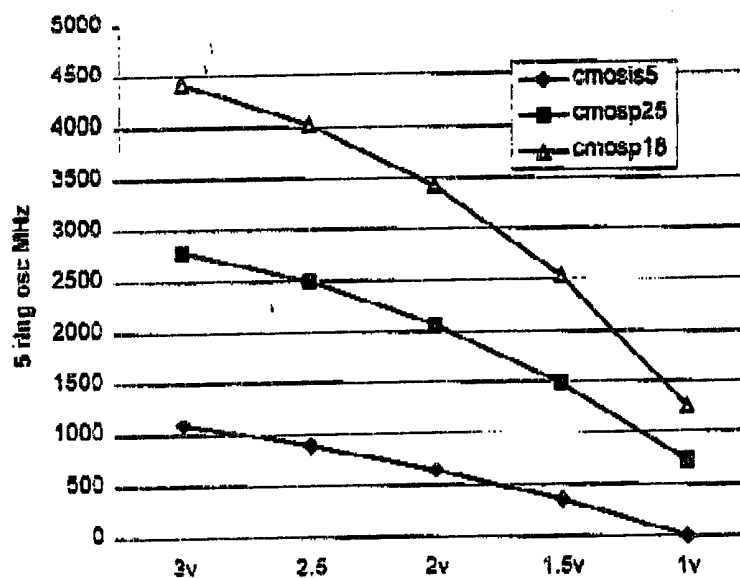


FIGURE 31